



Sir:

PATENT Attorney Docket No. 03180.0326-00000

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:		Group Art Unit: 2826
Tadashi IGUCHI )		Examiner: Quinto, Kevin V.
Application No.: 10/612,033		•
Filed: 、	July 3, 2003	Confirmation No.: 5852
) ) )	SEMICONDUCTOR MEMORY PREVENTING AN ELECTRIC SHORT CIRCUIT BETWEEN A WORD LINE AND A SEMICONDUCTOR SUBSTRATE, AND MANUFACTURING METHOD FOR THE SEMICONDUCTOR MEMORY (As Amended)	Mail Stop Amendment ) ) ) ) ) )
P.O. Bo	ssioner for Patents ox 1450 dria, VA 22313-1450	

## **AMENDMENT**

In reply to the Office Action mailed January 22, 2010, the period for response to which extends through April 22, 2010, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims in this paper.

Remarks follow the amendment section of this paper.